

REMARKS

Claims 1-3, 6, 12, 18-20, 24-27, 58-61, 69-84 and 91-92 are pending in the application. Claims 1-3, 6, 12, 18-20, 24-27, 58-61, 69-84 and 91-92 stand rejected under 35 U.S.C. 102(b) as being anticipated by *Chencinski et al.* (U.S. Patent No. 5,355,406). Applicants respectfully traverse. The rejection is wholly inadequate to prove a *prima facie* case. The examiner has ignored limitations in each of the rejected claims. For example, in claim 1, the examiner has not addressed the limitation that "the switching circuitry and the voice processing circuitry are controlled by not more than one microprocessor." Nevertheless, *Chencinski* does not teach or even suggest that the PBX and VMS are controlled by not more than one microprocessor. In fact, to the contrary, the reference states these are prior art components, which are undoubtedly each controlled by their own internal processor.

Regarding claims 3, 6, 12, 18, 20, 27, 58-61, 69-72, 76-77, 83-84 and 91, the examiner has not even specifically addressed the limitations within these claims.

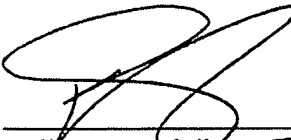
Regarding claims 19, 24-26, 59-61, 73-75, 78-82 and 92, the examiner has failed to address the limitations within each of these claims.

As a result, the examiner has failed to show how the reference anticipated the claims.

Please apply any necessary charges or credits to deposit account 06-1050.

Respectfully submitted,

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